

Featuring Semitransparent p–i–n Perovskite Solar Cells for High-Efficiency Four-Terminal /Silicon Tandem Solar Cell

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Abstract

Two issues need to be resolved when fabricating p–i–n semi-transparent perovskite solar cells (ST-PVSCs) for four-terminal perovskite/silicon tandem solar cells: (i) damage to the underlying absorber (MAPbI_3), electron transporting layer ([6,6]-phenyl- C_{60} -butyric acid methyl ester, PCBM), and work function modifier (polyethylenimine, PEI) resulting from the harsh sputtering conditions for the transparent electrodes (TEs) and (ii) low average near-infrared transmittance (ANT) of TEs. In this study, we developed a unique SnO_2 layer to protect the MAPbI_3 and PCBM layers, and function as a work function modifier for a new TE (cerium-doped indium oxide, ICO), which TE exhibits an excellent ANT of 86.7% in the range of 800-1200 nm. Moreover, we prepared a MAPbI_3 based p-i-n ST-PVSC, achieving an excellent power conversion efficiency (PCE) of 17.23%. When it places over the Si solar cell, a four-terminal tandem solar cell with a PCE of 26.14% is obtained.

Keywords: perovskite, silicon, tandem, conducting oxide, near-infrared, transmission, semitransparent

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1. Introduction

Organic and inorganic perovskite solar cells (PVSCs) are among the most promising emerging photovoltaic technologies for commercialization, due to their inexpensive solution processability and high power conversion efficiencies (PCEs). Recently, the highest certified PCE of a PVSC has reached 25.5%, very close to the record PCE of 26.7% for a single-crystalline silicon solar cell (c-Si SC).^[1,2] The PCEs of single-junction solar cells are, however, limited to 33%, according to theoretical calculations performed using the Shockley–Queisser model.^[3,4] To break through this PCE limitation of single-junction solar cells, it has been proposed to fabricate multi-junction solar cells (tandem) from absorbers having different band gaps. For example, in a two-junction solar cell, the architecture would feature of a wide-band-gap solar cell stacked on top of a low-band-gap solar cell. High-energy photons in the solar spectrum would be absorbed by the top cell, producing a high open-circuit voltage (V_{oc}); low-energy photons that are not be absorbed by the top cell could be absorbed by the bottom cell. This strategy can significantly minimize both thermalization and absorption losses, thereby increasing the degree of light harvesting and improving the PCE.^[4,5] Generally, the PCEs of multi-junction solar cells are enhanced upon the increasing number of junctions; even in the simplest case of two junctions, the theoretical PCE can be increased from approximately 33 to 42%.^[6]

The silicon solar cells (Si SCs) made from a band gap of 1.1 eV crystalline silicon have dominated the photovoltaics industry in the past few decades (market share > 90%), due to their adequate efficiency and reliability. In recent years, perovskites have been developed displaying optoelectronic characteristics suitable for highly efficient solar cells, including large optical absorption coefficients, long carrier diffusion lengths, and efficient ambipolar carrier transport.^[7–9] In addition, the band gaps of perovskite materials can be tuned readily in the range from 1.2 to 2.3 eV through compositional engineering; such tunable band gaps make these materials very suitable for combining with c-Si SCs to form tandem solar cells.^[10,11] Indeed, perovskite/silicon tandem solar cells have attracted a great deal of attention from researchers since 2015,^[12–15] with the combination of PVSCs and c-Si SCs as tandem cells easily breaking the PCE limit of single-junction solar cells with only a slight increase in cost.

Two architectures have been employed most commonly for perovskite/Si tandem solar cells: mechanically stacked four-terminal (4T) and monolithic two-terminal (2T) tandem devices. In the 2T configuration, the perovskite top cell is deposited on the silicon bottom cell and connected through an interconnection layer; this configuration requires only one transparent electrode (TE). The optical loss from this single TE is much lower than that from the three TEs in the 4T configuration, which features the top electrode of a Si SCs and the rear and front electrodes of



semitransparent perovskite solar cells (ST-PVSCs). Thus, the 2T configuration is generally expected to provide a PCE higher than that from the 4T configuration. It remains challenging, however, to uniformly place a perovskite layer on top of the textured structure of a Si SCs.^[16,17] In addition, it is necessary to ensure photocurrent matching between these two cells, because the photocurrent of a 2T tandem cell is limited by that of the sub-cell having the lower current.^[18] Therefore, the band gap of the perovskite top cell is an important parameter when optimizing the PCE of a 2T tandem cell. In contrast, in the 4T configuration, the perovskite top cell and the silicon bottom cell are fabricated independently and stacked together mechanically. Both cells are fabricated under their individually optimal conditions. Moreover, each of the two sub-cells is connected to its own tracking system, so there is no need for photocurrent matching between the two sub-cells of the 4T tandem device. Furthermore, because there is a considerable difference in stability between PVSC and Si SCs, the 4T configuration has another advantage in that it is possible to simply replace spent PVSCs.

When developing a 4T perovskite/silicon tandem cell, both the rear and front electrodes of the PVSCs must be transparent, functioning as semi-transparent PVSCs (ST-PVSCs). Many types of transparent electrodes have been applied to fabricate ST-PVSCs, including ultra-thin metal films,^[19,20] metal nanowires,^[21,22] carbon nanotubes,^[23,24] conducting polymers,^[25,26] and transparent conductive oxides (TCOs).^[27–29] Among them, TCOs are preferred because of their high transmittance, excellent chemical stability, high conductivity, and long term durability. TCO thin films are usually manufactured using sputtering processes. The following issues must be overcome, however, when manufacturing ST-PVSCs using sputtered TCO films. First, the deposition of energetic ions through sputtering can damage the soft materials beneath the rear TCO of a p–i–n device—namely, the work function (WF) modifier (polyethylenimine, PEI), the electron transport layer ([6,6]-phenyl-C₆₀-butyric acid methyl ester, PCBM), and the light absorption layer (the perovskite MAPbI₃). To protect the underlying layers, it is necessary to employ a buffer layer having good optoelectronic properties; a durable SnO₂ layer is a good candidate for p–i–n devices. However, most SnO₂ layers have been prepared using atomic layer deposition,^[30–34] which is complex and costly for mass production. Therefore, solution-processable SnO₂ buffer layers are highly desirable. Second, commercially available transparent electrodes [typically fluorine-doped tin oxide (FTO) and indium tin oxide (ITO)] have been used widely in the fabrication of ST-SCs, but usually they must be highly doped to achieve high conductivity. According to the Drude–Lorentz model, highly doped TCO electrodes suffer from free carrier absorption (FCA) in the near-infrared (NIR) range, caused by plasma oscillation and proportional to the free carrier density.^[35,36] The FCA effect induces a parasitic absorption in the NIR region, thereby decreasing the average



NIR transmittance (ANT) of the ST-PVSCs. Although minimizing the free carrier density of the TCO is an effective means of eliminating the FCA,^[36] the lower number of free carriers would also lower the conductivity (σ) of the TCO film, following the equation

$$\sigma = e\mu N_e$$

where e is charge per carrier, N_e is the carrier density, and μ is the carrier mobility. Therefore, a trade-off exists between high conductivity and a low FCA. Although many alternative TCO materials have been reported, there remains room for improving their performance. Sputtered Zn-doped In_2O_3 (IZO) exhibits a mobility of $50\text{--}60 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$,^[28,37] but it features a parasitic absorption in the ultraviolet region because of its amorphous nature. Although Zr-doped indium oxide (IZRO) transparent electrodes exhibit good carrier mobility (ca. $77 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$),^[29] it is less than that of the high-conductivity metal oxide ($>100 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$). The carrier mobility of hydrogen-doped In_2O_3 (IO:H) exceeds $100 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$,^[38] but its preparation includes the introduction of water vapor during the sputtering deposition. Precisely controlling the minute amounts of water vapor between batches can be challenging, potentially resulting in poor film quality. Furthermore, thermally induced diffusion of H_2 can make these films unstable at high temperature.^[39] Recently, cerium-doped indium oxide (ICO) has been used as the electrode material in silicon heterojunction (SHJ) solar cells because of its high carrier mobility and NIR transparency.^[40] Therefore, it would appear to be a good candidate also for use as the transparent electrode material in ST-PVSCs.

In this study we fabricated ST-PVSCs having the p-i-n structure TCO/ NiO_x /MAPbI₃/PCBM/buffer layer/TCO. We synthesized unique SnO_2 nanoparticles for use as the buffer layer, prepared through an inexpensive solution-coating process. This buffer layer performed dual functions: protecting the PCBM and perovskite layers from sputtering damage and tuning the WF to match the energy levels of PCBM and TCO without using PEI. We systematically varied the processing conditions of the ICO to achieve a high carrier mobility, a low free carrier density, and high NIR transmittance for both the front and rear TCO electrodes. Furthermore, we fabricated a MAPbI₃ based p-i-n ST-PVSC, having an outstanding PCE of 17.23% and an ANT of 76.8% (Table S1 and Figure S1). When we placed this ST-PVSC on top of a heterojunction (HJT) c-Si SC (23% PCE), we obtained a 4T tandem device displaying a PCE of 26.14%.

2. RESULTS AND DISCUSSION

Throughout this study we used devices having the p-i-n configuration front TCO/hole transport layer (HTL)/absorber/electron transport layer (ETL)/buffer layer/rear TCO because of its good



device stability.^[41] Here, the HTL was NiOx, the absorber was MAPbI₃, the ETL was PCBM, and the buffer layer was a film of tetrabutylammonium hydroxide-capped SnO₂ (TBAOH-SnO₂). The TCO was ITO, FTO, or ICO. The results of our systematic study of the buffer layer and TCO for the fabrication of ST-PVSCs are discussed below, along with the use of these ST-PVSCs in perovskite/Si 4T tandem solar cells.

2.1 Fabrication of semi-transparent PVSCs using solution-processable TBAOH-SnO₂ buffer layer

A robust metal oxide should be used as the buffer layer material to protect the underlying soft materials (PEI, PCBM and MAPbI₃) of p-i-n devices during the sputtering of the TCO electrode when preparing semi-transparent devices. SnO₂ is an n-type semiconductor material, suitable for depositing on top of the PCBM layer to facilitate electron transport in the device. In our previous study we found that a film of TBAOH-SnO₂ can perform dual functions: a durable n-type electron transport material and a WF-modifier for the metal electrode.^[42] In this present study, we used TBAOH-SnO₂ as the buffer layer of the sputtered TCO electrode and found that the TBAOH also exhibits the WF modification effect on the TCO electrode. The ITO rear electrode was the first TCO to be evaluated. Through simple spin-coating, the TBAOH-SnO₂ film was deposited on top of the PCBM layer. Therefore, the TBAOH ligand, which capped around the SnO₂ nanoparticles can form the molecular dipole-induced electrical field at the SnO₂-ITO interface, thereby modifying the WF of ITO. **Figure 1(a)** displays the molecular dipole of TBAOH, determined using ultraviolet photoelectron spectroscopy (UPS), revealing a WF shift of ITO from -4.67 to -3.69 eV. Thus, an ohmic contact was formed at the interface between TBAOH-SnO₂ and ITO to achieve efficient electron extraction, as illustrated in **Figure 1(b)**. The optimal sputtering conditions of ITO (50-W direct current (DC) power under 3 mtorr of Ar) were determined by varying the pressure of the Ar atmosphere and varying the DC power from 30 to 90 W. **Figure S2** reveals that the TBAOH-SnO₂ layer functioned well when the DC power reached up to 50 W, but the fill factor (FF) and open-circuit voltage (V_{oc}) of the ST-PVSC decreased greatly, as a result of destruction of the buffer layer, when the DC power was greater than 50 W. **Figure 1(c)** displays the device structure and cross-sectional scanning electron microscopy (SEM) image of the ST-PVSC. To demonstrate the dual functions of the TBAOH-SnO₂ layer for the sputtered ITO, for comparison we fabricated ST-PVSCs containing PEI as the organic WF modifier. **Table S2** summarizes the data from the $J-V$ curves of the opaque PVSCs and ST-PVSCs incorporating the PEI and TBAOH-SnO₂ layers. Both of the opaque PVSCs provided high efficiencies (>18%), indicating that the PEI and TBAOH-SnO₂ layers were both effective as WF modifiers. Nevertheless, the ST-PVSC incorporating PEI provided

a low PCE (1%) and a low FF (38%), consistent with the sputtered ITO damaging the soft material layers. In contrast, the ST-PVSC incorporating TBAOH-SnO₂ provided a good PCE (16.53%) and FF (75%) and exhibited negligible hysteresis (**Figure S3**). Thus, the TBAOH-SnO₂ layer protected the underlying materials during the sputter deposition process. To confirm that the TBAOH-SnO₂ layer could function as a WF modifier, we also tested the effects of pristine SnO₂ (i.e., without TBAOH capping) as a buffer layer in the ST-PVSC; **Figure S4** presents its $J-V$ curve. Interestingly, although the pristine SnO₂ layer protected the soft materials from damage during the sputtering process, the $J-V$ curve of the ST-PVSC featured an s-shaped kick, implying that charges accumulated at the SnO₂-ITO interface as a result of energy misalignment between the SnO₂ and ITO layers. Moreover, we performed electrical modeling to investigate the effect of the TCO sputtering process on the device performance. We employed drift-diffusion modeling on both the front and rear sides of the ST-PVSCs. The experimental data were in good agreement with the simulations, with a value of R^2 of 98.7% (**Figures S5**). The R^2 corresponds to goodness-of-fit from the regression analysis, and the value is in the range of 0% to 100%, depending on how well the experimental results match simulated data. Front-side illumination led to a higher PCE, because the front electrode featured lower parasitic optical absorption. **Table S3** lists the fitting parameters extracted from the modeling. Notably, even when the sputtered TCO was deposited on the ETL side, the defect density at the perovskite-ETL interface was half of that at the HTL-perovskite interface. Thus, TBAOH-SnO₂ provided very good protection and tunability of the WF of the sputtered TCO electrode, and confirmed the suitability of this unique dual-function TBAOH-SnO₂ layer for the manufacture of ST-PVSCs.

2.2 Development of highly NIR-transparent cerium-doped indium oxide

We have successfully fabricated ST-PVSCs featuring ITO as the rear electrode and FTO as the front electrode. Nevertheless, both FTO and ITO suffer from low NIR transmittance (at wavelengths ranging from 800 to 1200 nm) because of their high carrier densities. In this study, we replaced both of these TCO electrodes with 3 wt% cerium-doped indium oxide (ICO). The ionic radius of tetravalent cerium (87 pm) is very close to that of trivalent indium (80 pm), thereby minimizing lattice distortion near the dopant site. Therefore, the carrier mobility of ICO can be improved and its ANT can be maintained without heavy doping. The processing conditions for the rear electrode (r-ICO) were different from those for the front electrode (f-ICO), due to the difference in the materials underlying these ICOs. We discuss them separately, as follows.

For the preparation of the rear electrode, the ICO was deposited directly on top of the soft layers of the device. Therefore, we could not use a high sputter power or thermal annealing. We



chose to use DC power, instead of radio-frequency (RF) power, because it provided a higher deposition rate and less of a thermal effect at a fixed power (**Figure S6**). Nevertheless, DC sputtering leads to the issue of positive charge accumulation on the target surface, inducing a change in the film composition and generating oxygen vacancies in the TCO film.^[43,44] Although oxygen vacancies are required to ensure a high free carrier density, free carriers are strongly absorbed in the NIR region, resulting in low NIR transmittance. Therefore, we attempted to minimize the oxygen vacancies through passivation, by introducing a flow of oxygen during the sputtering process. The optimal oxygen flow rate was determined by varying the ratio of oxygen [$r(\text{O}_2) = \text{O}_2/(\text{Ar} + \text{O}_2)$] under a working pressure of 3 mtorr at a fixed film thickness of 300 nm. We used X-ray photoelectron spectroscopy (XPS), Hall effect measurements, and UV–Vis–NIR spectroscopy to characterize the ICO films in terms of their chemical compositions and chemical states, electronic properties, and optical properties, respectively. **Figure 2** and **Table 1** summarize the results. The chemical binding energies of In 3d_{5/2} and In 3d_{3/2} were located at 443.55 and 451.15 eV, respectively [**Figure S7(a)**]. The two peaks in the Ce 3d XPS spectrum were resolved into five Gaussian curves representing Ce⁴⁺ and Ce³⁺ oxidation states.^[45] The signals of Ce⁴⁺ were very strong, indicating that Ce⁴⁺ ions could replace In³⁺ ions in the lattice and generate free carriers [**Figure S7(b)**]. The O 1s XPS spectrum was resolved into three Gaussian components, with binding energies of 529.00, 530.55, and 531.45 eV, respectively [**Figure S7(c)**]. The peak with the lowest binding energy at 529.00 eV represents the bonded oxygen ions (O_I) adjacent to In atoms in the complete In₂O₃ lattice. The peak at 530.55 eV represents the O²⁻ ions near the oxygen-deficient region in the In₂O₃ lattice (O_{II}); it can be related to oxygen vacancies. The peak at 531.45 eV represents the interstitial oxygen ions in the In₂O₃ lattice or the OH groups on the surface (O_{III}). **Figure 2(a)** reveals the effect of the oxygen flow ratio on the composition of O 1s in the r-ICO film. As the value of $r(\text{O}_2)$ increased from 0 to 0.27%, the ratio of oxygen-deficient regions to total oxygen (O_{II}/O_{total}) decreased from 36.21 to 34.30%. Thus, by introducing oxygen during the sputter deposition process, the amount of oxygen vacancies could be minimized effectively through oxygen passivation.^[46,47] Furthermore, we observed that when the value of $r(\text{O}_2)$ increased to 0.27%, the ratio of interstitial oxygen to total oxygen (O_{III}/O_{total}) increased to 23.4% because of the presence of oxygen ions in the lattice.^[48,49] **Figure 2(b)** reveals that the free carrier density (N_e) of the ICO decreased upon increasing the value of $r(\text{O}_2)$, thereby improving the ANT of the ICO film. When the value of $r(\text{O}_2)$ reached 0.13%, the mobility (μ) of the film increased as a result of the passivation of oxygen vacancies, as described above. When we increased the value of $r(\text{O}_2)$ further to 0.27%, the interstitial oxygen that formed also acted as a scattering factor and decreased the carrier mobility. In short, applying an optimal value of $r(\text{O}_2)$ of 0.13% provided the r-ICO with a lower



sheet resistance ($28.75 \Omega \text{ sq}^{-1}$; $N_e = 2.34 \times 10^{20} \text{ cm}^{-3}$; $\mu_e = 32.51 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$) and higher ANT (80%) than those of the other r-ITO samples. Accordingly, we used this ICO as the rear electrode for the ST-PVSCs prepared throughout the remainder of this study.

Because the f-ICO electrode was prepared on a glass substrate, there were no strict limitations on its sputtering power or processing temperature. Therefore, we chose RF sputtering to prepare the f-ICO, because it occurs with fewer composition variations (**Figure S7**) and provides better film quality (**Figure S8**) when compared with DC sputtering. We obtained the 300-nm f-ICO film by applying a high power of 240 W under an Ar atmosphere with a working pressure of 3 mtorr. To improve the film quality after sputtering, we performed annealing at various temperatures. We used X-ray diffraction (XRD), SEM, and XPS to evaluate the film characteristics. The as-deposited f-ICO film exhibited a broad XRD diffraction signal [**Figure 3(a)**], implying that it was amorphous. The crystallinity of the f-ICO film improved upon increasing the annealing temperature. For the f-ICO film annealed at 400 °C, the (222) diffraction peak appeared at a high diffraction angle, indicating that its lattice was denser than those of the f-ICO films annealed at lower temperatures. This high annealing temperature promoted the rearrangement of atoms by adjusting the lattice alignment and minimizing defects, thereby forming denser crystals. The SEM image of the f-ICO film annealed at 200 °C revealed that new crystal grains had formed and were distributed in the amorphous matrix; when the annealing temperature was increased to 400 °C, the crystals shrank slightly to cause a larger grain boundary and a denser film, consistent with the XRD analyses. The XPS pattern of the as-deposited ICO film prepared through RF sputtering was different from that of the aforementioned DC-sputtered film (**Figure S7**). **Figure S9** presents the O 1s XPS spectra of the f-ICO films annealed at the various temperatures. Upon increasing the annealing temperature from room temperature to 400 °C, the O_{II}/O_{tot} ratio decreased accordingly from 33.62 to 24.33%, indicating that the content of oxygen vacancies also decreased accordingly. **Table 2** and **Figure 3(c)** summarize the electrical properties of the f-ICO films annealed at the various temperatures. Upon increasing the annealing temperature, the free carrier density decreased because of a decrease in the number of oxygen vacancies, as discussed above for the DC-sputtered films. Interestingly, the carrier mobility increased initially and then decreased upon increasing the annealing temperature. Because an ordered structure provides less of a scattering effect, it is known that the carrier mobility will increase upon improving the crystallinity. In addition to the crystallinity, the mobility is also determined by the grain boundary scattering effect. The grain boundary of the f-ICO film annealed at 400 °C was coarse, as revealed in its SEM image [**Figure 3(b)**], thereby intensifying the grain boundary scattering effect and decreasing the carrier mobility. Thus, the film annealed at 300 °C exhibited the best conductivity. **Figure 3(d)** presents the optical band gaps of

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the annealed films. The band gap of the f-ICO film increased upon increasing the annealing temperature, due to improved film crystallinity. Therefore, the optical absorption in the ICO film underwent a blue-shift, significantly increasing the average visible light transmittance (AVT). **Figure 3(e)** displays the ANTs of the ICO films annealed at the various temperatures. The ANT of the as-deposited ICO film (82.94%) increased to 86.72% after annealing at 300 °C, but decreased slightly to 86.65% after annealing at 400 °C. The improvement in the ANT was due to the decrease in the free carrier density. Compared with the commercial f-FTO, each of our f-ICO films in this study featured a lower carrier density, a higher mobility, and a higher ANT. The f-ICO film annealed at 300 °C exhibited the best performance: a low sheet resistance of $9.99 \Omega \text{ sq}^{-1}$ ($N_e = 2.49 \times 10^{20} \text{ cm}^{-3}$; $\mu_e = 104.7 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$), a high AVT of 82.45%, and an ANT of 86.72%. Therefore, we used the f-ICO film annealed at 300 °C as the front electrode of ST-PVSCs for the fabrication of perovskite/silicon tandem cells.

2.3 Highly NIR-transparent PVSCs and four-terminal perovskite/silicon tandem solar cells

We investigated the effects of the various TCO on the performance of ST-PVSCs (**Figure 4, Table 3**). The devices denoted “f-FTO/r-ITO” and “f-ICO/r-ICO” were fabricated from combinations of f-FTO/r-ITO and f-ICO/r-ICO electrodes, respectively. **Figure 4(a)** displays a schematic representation of the tandem solar cells and a cross-sectional SEM image of the ST-PVSC fabricated from the f-ICO and r-ICO electrodes. The PCE of the f-FTO/r-ITO device (16.5%) was lower than that of the f-ICO/r-ICO device (17.2%), resulting from the AVT of f-ICO (82.5%) being higher than that of f-FTO (81%). In addition, we found that the ST-PVSCs with f-ICO/r-ICO showed a higher series resistance (R_s) ($5.51 \Omega \cdot \text{cm}^2$) than the devices with f-FTO/r-ITO ($5.01 \Omega \cdot \text{cm}^2$), resulting in the lower FF. Since all the devices have the same ETL, perovskite and HTL, the main affecting factor for the R_s should be the resistance of the transparent electrodes. Therefore, the FF of the f-ICO/r-ICO device was lower than that of the f-FTO/r-ITO device, due to the sheet resistance of f-ICO (ca. $10 \Omega \text{ sq}^{-1}$) being higher than that of f-FTO (ca. $7 \Omega \text{ sq}^{-1}$). **Figure 4(b)** reveals the low variation in the photovoltaic performance of the different batches of ST-PVSCs, indicating the good reproducibility of the device fabrication process. We employed UV–Vis–NIR spectroscopy to explore the influence of the different TCOs on the ANTs of the ST-PVSCs [**Figure 4(c)**]. The ANT of the ST-PVSC improved from 53.73 to 76.74% after replacing the electrode combination from f-FTO/r-ITO to f-ICO/r-ICO, mainly due to the lower FCAs of both the f-ICO and r-ICO electrodes. Thus, more NIR light could pass through the perovskite top cell and be used by the silicon bottom cell, thereby improving the performance of the perovskite/silicon tandem solar cells.

We stacked the ST-PVSC mechanically on top of the silicon bottom cell to fabricate 4T perovskite/silicon tandem solar cells. Because the perovskite top cell mainly absorbed visible light, it functioned as a filter for the HJT c-Si SC, with only NIR light being absorbed by the HJT c-Si bottom cell. **Table 3** and **Figure 4d** summarize the performance and J - V curves of the stand-alone HJT c-Si SC, perovskite top cell, and filtered HJT c-Si bottom cell. The PCE of the stand-alone HJT c-Si SC was approximately 23%, with a short-circuit current density (J_{sc}) of 40.5 mA cm^{-2} , a value of V_{oc} of 0.72 V, and a FF of 78.79%. The HJT c-Si bottom cells filtered by the ST-PVSCs having f-FTO/r-ITO and f-ICO/r-ICO electrodes provided values of J_{sc} of 13.13 and 16.00 mA cm^{-2} , respectively. This improvement in short-circuit current density arose from the ANT of the f-ICO/r-ICO device being higher than that of the f-FTO/r-ITO device, as we discussed in the previous section. Accordingly, the PCEs of the HJT c-Si bottom cells filtered by the ST-PVSCs having f-FTO/r-ITO and f-ICO/r-ICO electrodes were 7.00 and 8.90%, respectively. Figure 4e presents the EQE spectra of the various ST-PVSCs, stand-alone HJT c-Si SC, and filtered HJT c-Si bottom cell. The integrated value of J_{sc} from the EQE spectrum of each cell was consistent with the value measured from its J - V curve. As a result, the PCEs of the 4T perovskite/silicon tandem cells with ST-PVSCs fabricated using f-FTO/r-ITO and f-ICO/r-ICO electrodes were 23.53 and 26.14%, respectively. The PCEs of all the tandem devices were higher than that of the stand-alone HJT c-Si SC. By replacing the electrode combination from f-FTO/r-ITO to f-ICO/r-ICO, the PCEs of the perovskite top cell and silicon bottom cell increased by 0.7 and 1.90%, respectively. Thus, the PCE of the mechanically stacked 4T perovskite/silicon tandem solar cell reached 26.14%. In summary, ICO is a very good transparent electrode material for ST-PVSCs when fabricating tandem solar cells with Si solar cells.

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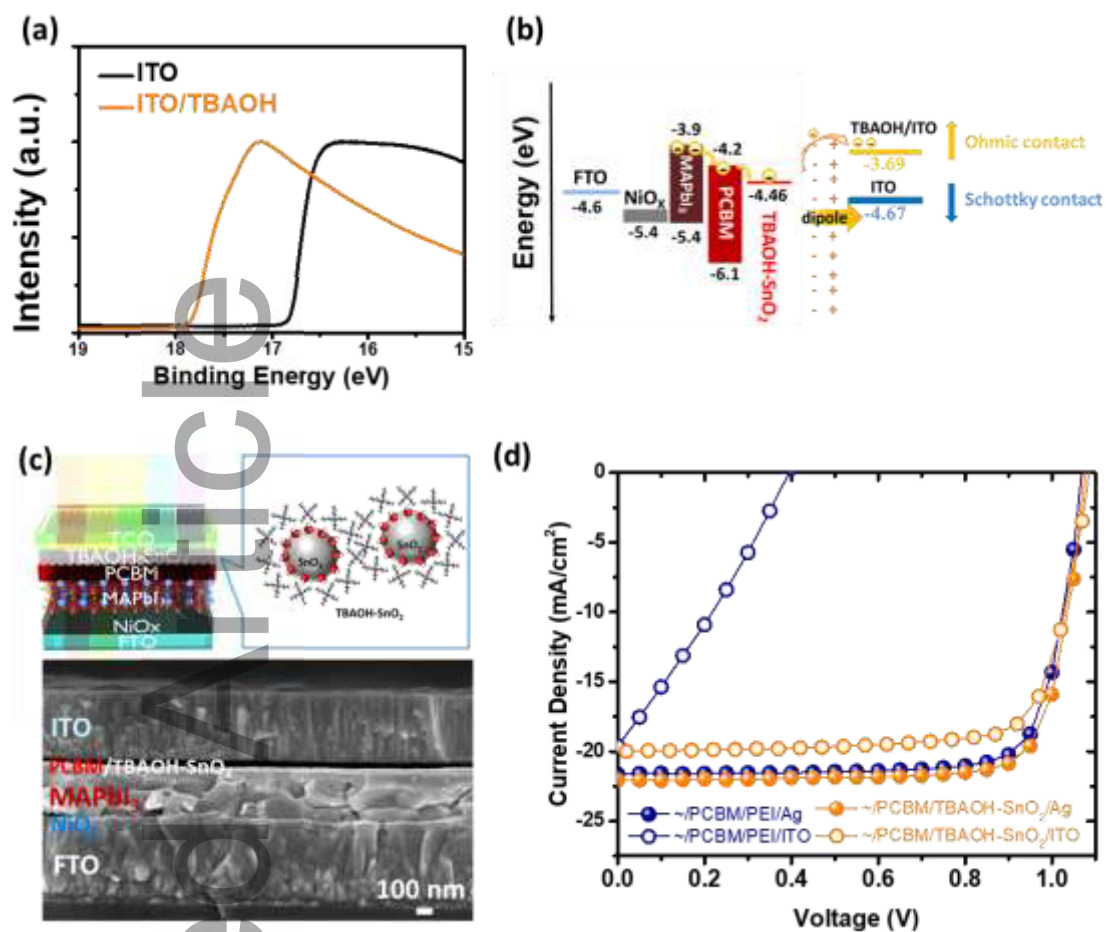


Figure 1 (a) UPS spectra of ITO and TBAOH-modified ITO. (b) Energy band diagram of ST-PVSCs prepared with and without TBAOH-SnO₂. (c) Schematic representation and cross-sectional SEM image of a ST-PVSC. (d) J - V curves of opaque PVSCs and ST-PVSCs fabricated from PCBM/PEI and PCBM/TBAOH-SnO₂.

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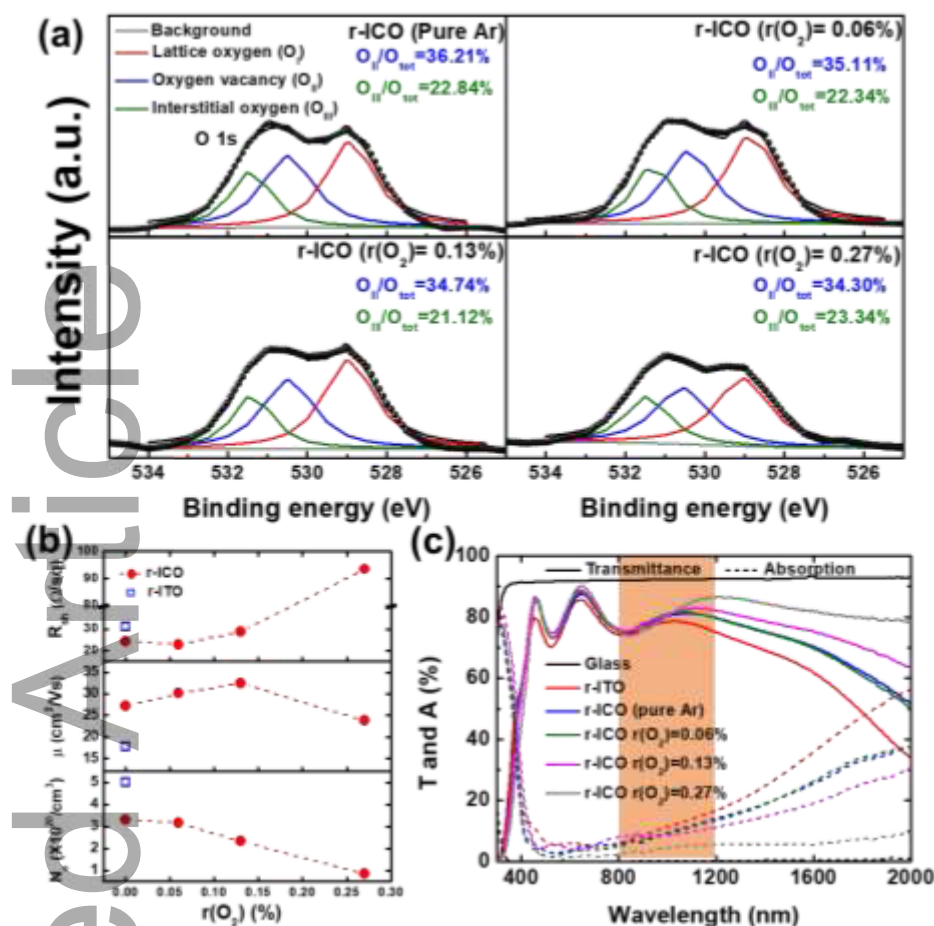


Figure 2 (a) O 1s XPS spectra, (b) sheet resistances, carrier mobilities, and free carrier densities, and (c) UV–Vis–NIR transmittance and absorption spectra of r-ICO electrodes prepared under various oxygen flow ratios during sputter-deposition.

Table 1 Effect of the value of $r(\text{O}_2)$ on the sheet resistance, carrier mobility, free carrier density, and ANT of r-ICO electrodes.

Rear electrode	Oxygen fraction $r(\text{O}_2)$ (%)	Sheet resistance R_{sh} ($\Omega \text{ sq}^{-1}$)	Carrier mobility μ ($\text{cm}^2 \text{ V}^{-1} \text{ s}^{-1}$)	Free carrier density N_e ($\times 10^{20}/\text{cm}^3$)	ANT (800–1200 nm) (%)
r-ITO	0	31.28 ± 1.21	17.77	5.02	76.77
	0	24.11 ± 0.51	27.13	3.31	79.02
r-ICO	0.06	22.98 ± 0.57	30.14	3.17	79.37
	0.13	28.75 ± 0.94	32.51	2.34	80.00
	0.27	93.51 ± 10.82	23.80	0.85	80.24



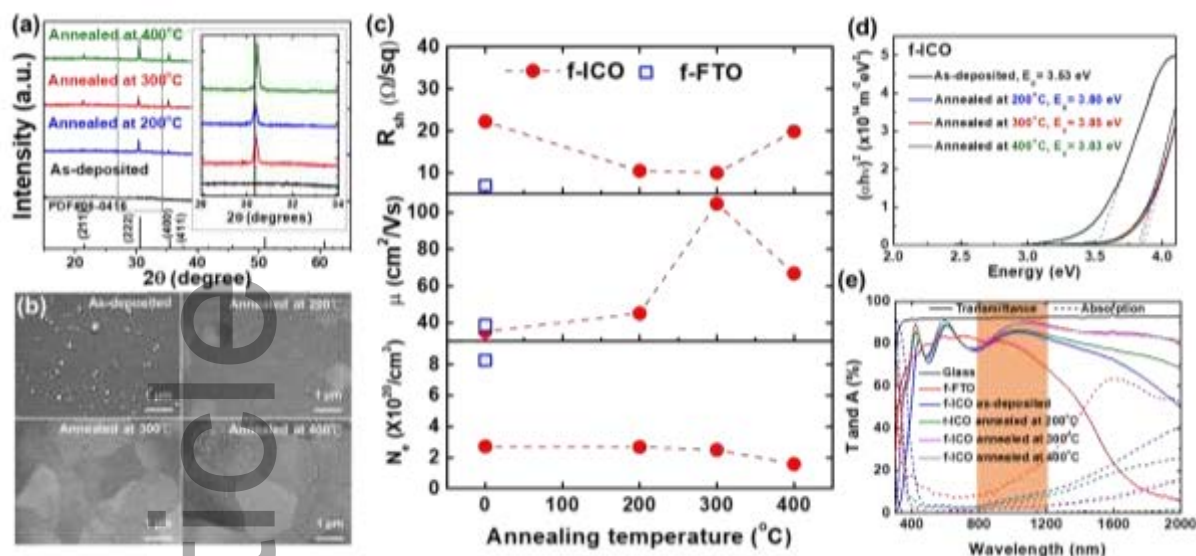


Figure 3 (a) XRD patterns [whole view and enlarged view of the (2 2 2) peak], (b) SEM top view images, (c) sheet resistances, carrier mobilities, and free carrier densities, (d) optical band gaps (E_g) calculated using the Tauc plot method, and (e) UV–Vis–NIR transmission and absorption spectra of f-ICO electrodes annealed under vacuum at various annealing temperatures.

Table 2 Effect of annealing temperature on the sheet resistance, carrier mobility, free carrier density, AVT, and ANT of f-ICO electrodes.

Front electrode	Annealing temperature (°C)	Sheet resistance, R_{sh} ($\Omega \text{ sq}^{-1}$)	Carrier mobility, μ ($\text{cm}^2 \text{ V}^{-1} \text{ s}^{-1}$)	Free carrier density, N_e ($\times 10^{20}/\text{cm}^3$)	AVT (400–800 nm) (%)	ANT (800–1200 nm) (%)
f-FTO	–	ca. 7	38.62	8.27	81.02	77.69
	(as-deposited)	22.20 ± 1.54	32.55	3.13	79.11	82.94
f-ICO	200	10.74 ± 0.68	82.21	2.56	80.87	84.02
	300	9.99 ± 0.94	104.70	2.49	82.45	86.72
	400	19.81 ± 3.92	70.79	1.91	82.18	86.65

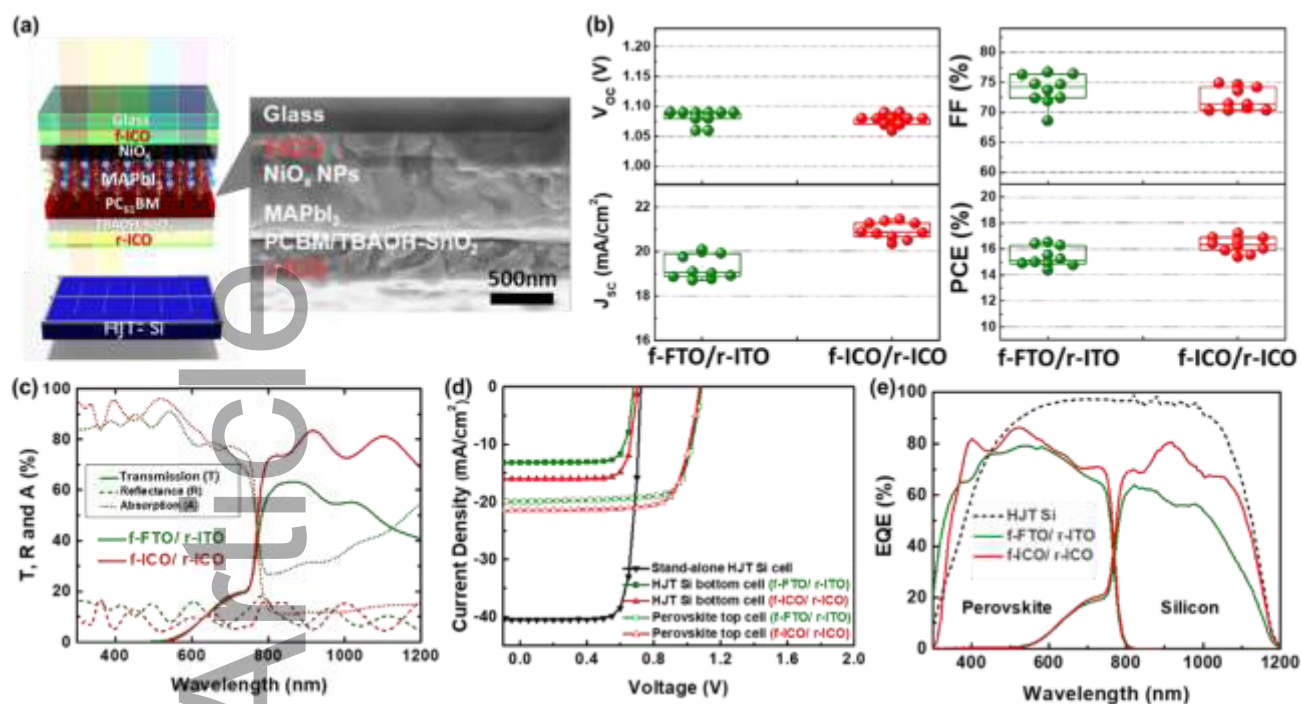


Figure 4 (a) Schematic representation of a 4T perovskite/silicon tandem solar cell and cross-sectional SEM image of an ST-PVSC fabricated using f-ICO/r-ICO electrodes. (b) Distribution of photovoltaic parameters, (c) UV-Vis-NIR transmission, reflection, and absorption spectra of ST-PVSCs with f-FTO/r-ITO and f-ICO/r-ICO electrodes. (d) $J-V$ curves and (e) EQE spectra of a stand-alone HJT silicon solar cell, perovskite top cells with various combinations of electrodes, and HJT silicon bottom cells.

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Table 3 Device performance of perovskite top cells with various combinations of electrodes, a stand-alone HJT silicon solar cell, HJT silicon bottom cells, and 4T perovskite/silicon tandem solar cells. Average data obtained from at least 12 devices. Photovoltaic parameters of the champion cell are provided in parentheses.

Device	V_{oc} (V)	J_{sc} (mA cm ⁻²)	FF (%)	PCE (%)
Perovskite top cell f-FTO/r-ITO	1.09 ± 0.01 (1.09)	19.54 ± 0.57 (19.92)	74.59 ± 2.79 (76.44)	15.83 ± 0.63 (16.53)
Perovskite top cell f-ICO/r-ICO	1.08 ± 0.01 (1.08)	20.93 ± 0.42 (21.44)	73.63 ± 1.50 (74.66)	16.64 ± 0.57 (17.23)
Stand-alone HJT Si cell	0.72 ± 0.01 (0.72)	40.12 ± 0.33 (40.50)	78.81 ± 0.37 (78.79)	22.68 ± 0.31 (23.07)
HJT Si bottom cell f-FTO/r-ITO	0.68 ± 0.01 (0.68)	12.82 ± 0.15 (13.13)	78.19 ± 0.53 (78.30)	6.85 ± 0.10 (7.00)
HJT Si bottom cell f-ICO/r-ICO	0.70 ± 0.00 (0.70)	15.72 ± 0.05 (16.00)	78.91 ± 0.15 (79.46)	8.69 ± 0.04 (8.90)
4T tandem solar cell f-FTO/r-ITO	-	-	-	22.83 ± 0.63 (23.53)
4T tandem solar cell f-ICO/r-ICO	-	-	-	25.56 ± 0.57 (26.14)

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3. Conclusion

We have fabricated highly efficient ST-PVSCs having the p-i-n structure TCO/NiO_x/MAPbI₃/PCBM/buffer layer/TCO. We synthesized unique SnO₂ nanoparticles to fabricate the buffer layer using an inexpensive solution-coating process. Use of this new buffer layer avoided damage to the underlying soft materials during the sputter processing of the TCO. The buffer layer not only protected the soft materials PCBM and MAPbI₃ but also tuned the WF of the TCO in line with that of the ETL, without the need for PEI. We systematically investigated the optimal processing conditions to obtain ICO as a new type of TCO. This ICO exhibited a high carrier mobility, a low free carrier density, and a high NIR transmittance suitable for the fabrication of semitransparent perovskite solar cells. The rear ICO electrode (r-ICO) was used to replace ITO, processed through DC sputtering with a low thermal effect; it displayed a carrier mobility of 32.51 cm² V⁻¹ s⁻¹, a free carrier density of 2.34×10^{20} cm⁻³, a sheet resistance of 28.75 Ω sq⁻¹, and an ANT of 80%. The front ICO electrode (f-ICO), used to replace FTO, was processed through RF sputtering and annealed at 300 °C to improve its performance; it displayed a carrier mobility of 104.7 cm² V⁻¹ s⁻¹, a free carrier density of 2.49×10^{20} cm⁻³, a sheet resistance of 9.99 Ω sq⁻¹, and an ANT of 86.72%. When we used ICO for both the front and rear electrodes, the ANT of device increased significantly, from 53.73 to 76.74%, thereby enhancing the PCE of the 4T p-i-n perovskite/silicon tandem cell from 23.53 to 26.14%. Furthermore, the development of the TBAOH-SnO₂ buffer layer and ICO electrodes in this study has a potential for application also to the fabrication of high-performance 2T perovskite/silicon tandem cells.

4. Experimental section

Materials

Chlorobenzene (CB, >99.0%), *N,N*-dimethylformamide (DMF, 99.8%), dimethyl sulfoxide (DMSO, >99.9%), and isopropyl alcohol (IPA, 99.8%) were purchased from Acros Organics. Diethyl ether (99.0%) and ethyl alcohol (EtOH 99.99%) were purchased from Fisher Chemical. PEI (branched; *M_w* = ca. 25,000) was purchased from Alfa Aesar. FTO glasses, 97 wt% In₂O₃ and 3 wt% CeO₂ sputter target (ICO), 90 wt% In₂O₃ and 10 wt% SnO₂ (ITO), methylammonium iodide (MAI), lead iodide (PbI₂, 99.9985%) and PC₆₁BM (99.0%) were purchased from FrontMaterials. Glass substrates (Corning Eagle XG) were purchased from Corning.

Preparation of TBAOH-SnO₂ solution

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SnO₂ NPs were synthesized using a solvothermal method. The precursor solution was prepared by adding SnCl₄ (3.12 g, 0.012 mol) to a mixed solvent of benzyl alcohol and toluene (3:1, v/v; 20 mL). This precursor solution was transferred to a 25-mL Teflon-lined autoclave and heated at 180 °C for 12 h in an oven. The white precipitate was collected by centrifugation and washed once with diethyl ether and twice with EtOH. To prepare the oleic acid (OA)–SnO₂ suspension, the collected SnO₂ NPs were redispersed in CHCl₃ (5 mL) and OA (1 mL) was added to form a milky suspension. During ultrasonication, butylamine (BA, 1 mL) was added to obtain a colorless transparent suspension. The OA–SnO₂ suspension was purified by adding acetone to wash out excess OA and BA, followed by centrifugation and re-dispersion in CHCl₃ at a concentration of 200 mg mL⁻¹. To remove any OA molecules attached to the surface of the SnO₂ NPs, the OA–SnO₂ NP suspension (50 μL) was added into a solution of 8 mM boron trifluoride etherate (BF₃–OEt₂) in CH₂Cl₂ (3 mL). The SnO₂ NPs that precipitated immediately were collected by centrifugation. To obtain the TBAOH-capped SnO₂ NP suspension, the collected SnO₂ NPs were added into a solution containing TBAOH (30 mg) and EtOH (1 mL) and then subjected to ultrasonication for several minutes, giving a colorless suspension. The TBAOH-SnO₂ NPs were precipitated by adding hexane to wash out the excess TBAOH. The precipitated SnO₂ NPs were re-dispersed in EtOH. The TBAOH-SnO₂ NP suspension at a concentration of 10 mg mL⁻¹ was a well-dispersed, stable, and clear solution.

Deposition of the rear ITO electrode

A target material comprising 90 wt% In₂O₃ and 10 wt% SnO₂ (SnO₂:In₂O₃, ITO, FrontMaterials) was used to prepare the ITO rear electrode (r-ITO). The samples were transferred into the chamber of a sputtering machine; the chamber was evacuated until the pressure was less than 2×10^{-6} torr, to eliminate the excess oxygen and moisture. [Note: To confirm their electrical and optical properties, the r-ITO films were deposited on soda lime glass (Corning Eagle XG)]. Pure Ar was used as the processing gas. To prevent destruction of the perovskite layer and the PC₆₁BM ETL from high-energy particles and high temperature environments, the r-ITO electrode was deposited through DC sputtering (Kao Duen Tec. Co.) with a low sputter power of 50 W under a working pressure of 3 mtorr.

Deposition of the rear ICO electrode

A target material with 97 wt% In₂O₃ and 3 wt% CeO₂ (CeO₂:In₂O₃, ICO, FrontMaterials) was used to prepare the ICO rear electrode (r-ICO). The samples were transferred into the chamber of a sputtering machine; the chamber was evacuated until the pressure was less than 2×10^{-6} torr, to



eliminate excess oxygen and moisture. Pure Ar and an O₂/Ar (1:99, v/v) gas mixture were used as processing gases. The r-ICO electrode was deposited through DC sputtering (Kao Duen Tec. Co.) with the same conditions as those used for ITO sputtering. Atmospheres with different oxygen flow to total flow ratios [$r(\text{O}_2) = \text{O}_2/(\text{Ar} + \text{O}_2)$] were applied during the sputtering process to optimize the electrical and optical properties of the r-ICO electrode. The optimized value of $r(\text{O}_2)$ was 0.13%, obtained by controlling the flow rates of the pure Ar and O₂/Ar mixture at 26 and 4 sccm, respectively.

Deposition of the front ICO electrode

A target material comprising 97 wt% In₂O₃ and 3 wt% CeO₂ was used to prepare the ICO front electrode (f-ICO). The f-ICO electrode was deposited on top of Corning glass. Prior to use, the glass substrates were cleaned sequentially with ammonia, hydrogen peroxide, deionized water, methanol, and isopropanol, then treated with oxygen plasma for 15 min to remove organic residues from the surface. The samples were transferred into the chamber of a sputtering machine; the chamber was evacuated until the pressure was less than 2×10^{-6} torr to eliminate excess oxygen and moisture. Pure Ar was used as the processing gas (flow rate: 35 sccm). The ICO front electrode was then deposited through RF sputtering (Kao Duen Tec. Co.) with a sputter power of 240 W under a working pressure of 3 mtorr. The deposition rate of the ICO front electrode was approximately 0.11 nm s⁻¹. After deposition, the samples were transferred to a tubular furnace and annealed under vacuum at 300 °C for 30 min with a heating rate of 5 °C min⁻¹.

Fabrication of opaque and semi-transparent perovskite solar cells

f-FTO glass (or f-ICO glass) was washed ultrasonically sequentially with acetone, methanol, and isopropanol. The NiO_x NP HTL layer (20 mg mL⁻¹ in DI water) was spin-coated onto clean FTO (or f-ICO) glass at 2500 rpm for 60 s, then annealed at 160 °C for 30 min in air. The NiO_x NPs were synthesized according to a previously published procedure.^[50] A 1.2 M solution of the perovskite precursor was prepared by mixing MAI and PbI₂ (1:1 molar ratio) in DMF and DMSO (5:2, v/v; 1 mL). This solution was spin-coated onto the as-prepared NiO_x film at 4500 rpm for 30 s in a glove box filled with N₂. After spinning for 15 s, diethyl ether (300 μL) was added to the sample to wash out the extra solvent and form a transparent intermediate perovskite phase. All samples were then annealed on a hot plate—at 70 °C for 1 min and then at 100 °C for 2 min—to form dark-brown perovskite films. A PC₆₁BM solution (2.5 wt.% in CB) was spin-coated onto the perovskite layer at 1000 rpm for 30 s. Finally, the TBAOH-SnO₂ buffer layer was spin-coated onto the PC₆₁BM layer at 1500 rpm for 30 s. To prepare the opaque device, a 100-nm-thick Ag rear

electrode was deposited through thermal evaporation onto PCBM/TBAOH-SnO₂ with area of 0.09 cm². To prepare the semi-transparent device, the r-ICO (or r-ITO) electrode was deposited onto the ETL with an area of 0.09 cm² through DC sputtering, as described above. Note that the ST-PVSCs filter was made on a 2.5 × 2.5 cm² substrate with full coverage of the transparent electrode. The LiF AR layer was deposited through thermal evaporation at 1 × 10⁻⁶ torr. For the first 20 nm, the deposition rate was controlled at 0.5 Å s⁻¹; for the rest of the thickness, the deposition rate was increased to 1 Å s⁻¹.

Fabrication and measurement of 4T perovskite/silicon tandem solar cells

The PCEs of individual silicon solar cells and the ST-PVSCs were measured over an aperture area of 4 cm² and 0.09 cm², respectively. For the PCE measurements of 4T perovskite/silicon tandem solar cells, we placed a 2.5 cm × 2.5 cm perovskite optical filter with the same ST-PVSCs architecture on top of the silicon solar cell. After covering with the perovskite optical filter, the filtered silicon bottom cell was measured over an aperture area of 4 cm². Measurements were performed using a voltage source meter (Keithley 2410) under an AM 1.5G solar simulator (Yamashita Denso) with irradiation at 100 mW cm⁻². The detail of measurement procedure is presented graphically as shown in **Figure S10**. The PCEs of the 4T perovskite/Si tandem solar cells were calculated by adding together the PCEs of the perovskite top cell and the filtered silicon bottom cell. In this study, a heterojunction silicon solar cell (HJT c-Si) was used as the bottom cell, supplied by Union Renewable Energy Company in Taiwan.

Electronic modeling of device

The drift–diffusion model was applied to quantitatively simulate the perovskite solar cells. It describes the generation, transport, and recombination processes, using the following continuous equations for electrons and holes:

$$\frac{\partial n}{\partial t} = G - R + \frac{1}{q} \nabla \cdot J_n, \quad \frac{\partial p}{\partial t} = G - R - \frac{1}{q} \nabla \cdot J_p,$$

where n and p are the electron and hole densities, respectively; G is a light-dependent generation rate of charge carriers; R represents all the recombination processes (Shockley–Read–Hall, modified Langevin and Auger models); q is the elementary charge constant; and J_n and J_p are the electron and hole drift-diffusion currents, respectively. The continuous equations were connected with the spatial electric field Poisson equation:

$$\nabla \cdot F = \frac{q}{\epsilon_0 \epsilon_r} (p - n + N_D - N_A),$$

where F is spatial electric field distribution, ϵ_0 is the vacuum permittivity constant, and ϵ_r represents the permittivity of the layer. The model was discretized using the Scharfetter–Gummel method together with Chebyshev polynomials to apply a nonlinear spatial grid. To simulate the J – V characteristics, the model was solved by forward iteration in time to find the steady-state current for each applied voltage. To avoid neglecting the transportation losses due to energy differences between layers, the generalized potentials method was applied. Further details of the model have been reported previously.^[51]

Characterization of materials and devices

The WFs of the films were measured using ultraviolet photoelectron spectroscopy (PHI 5000 VersaProbe, ULVAC-PHI) with an ultraviolet light source of He I emission (21.2 eV, B50 W), and calculated using the equation

$$\Phi = h\nu - (E_0 - E_F)$$

XPS spectra were recorded using a PHI 5000 Versa Probe system (ULVAC-PHI, Chigasaki) and a micro-focused (100 μm , 25 W) Al X-ray beam. The transmission (T) and reflection (R) spectra of transparent conductive oxide films and ST-PVSCs were measured using UV–Vis–NIR spectroscopy (JASCO, V-770). The measurements were performed in the range 300–1200 nm with a step of 1 nm. The absorbances (A) of the films or ST-PVSCs were calculated using the equation

$$A = 100 - T - R$$

The free carrier densities and carrier mobilities of the transparent electrodes were determined through Hall effect measurements (ECOPIA, HMS-3000). Top-view and cross-sectional images of the devices were recorded using SEM (JEOL, JSM 7610FPLUS). The crystal structures of the f-ICO films were determined through XRD (Rigaku, TTRAX III) with Cu $K\alpha$ ($\lambda = 0.154$ nm) radiation at 18 kV and over the range 15–65°. The J – V curves of devices having an active area of 0.09 cm^2 were measured through both forward (from –0.2 to +1.2 V) and backward (from +1.2 to –0.2 V) scans. The step voltage of the test and the delay time were 50 mV and 10 ms, respectively. Measurements were performed using a voltage source meter (Keithley 2410) under an AM 1.5G solar simulator (Yamashita Denso) with irradiation at 100 mW cm^{-2} . ND filters were used to achieve lower light intensity. All of the devices were measured in air. The HJT c-Si solar cell having a size of 2 \times 2 cm^2 was provided by United Renewable Energy (Taiwan). For the J – V measurements of the 4T-perovskite/silicon tandem solar cell, a 2.5 \times 2.5 cm^2 ST-PVSCs filter was stacked on top of the HJT c-Si cell for measurement of the efficiency of the silicon bottom cell. The EQE spectra of the solar cell were measured using an EQE measurement system (Enlitech Taiwan, QE-R3011). Prior to measurement, the EQE system was calibrated using a silicon and germanium

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diode. The HJT c-Si solar cell was filtered by a $2.5 \times 2.5 \text{ cm}^2$ ST-PVSCs filter during EQE measurement.

Supporting Information

Supporting Information is available from the Wiley Online Library or from the author.

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Featuring Semitransparent p–i–n Perovskite Solar Cells for High-Efficiency Four-Terminal /Silicon Tandem Solar Cell

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In this study, we developed a unique SnO₂ layer to protect the under-laying layers from damage of sputtered transparent electrode. Moreover, we prepare a high NIR-transparent perovskite solar cell by using cerium doped indium oxide achieving a record power conversion efficiency (PCE) of 17.23%. As a result, a four-terminal perovskite/silicon tandem solar cell with a PCE of 26.14% is obtained.



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