







Article

Thermal Instability of Gold Thin Films

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Abstract: The disintegration of a continuous metallic thin film leads to the formation of isolated islands, which can be used for the preparation of plasmonic structures. The transformation mechanism is driven by a thermally accelerated diffusion that leads to the minimalization of surface free energy in the system. In this paper, we report the results of our study on the disintegration of gold thin film and the formation of nanoislands on silicon substrates, both pure and with native silicon dioxide film. To study the processes leading to the formation of gold nanostructures and to investigate the effect of the oxide layer on silicon diffusion, metallic film with a thickness of 3 nm was deposited by molecular beam epitaxy (MBE) technique on both pure and oxidized silicon substrates. Transformation of the thin film was observed by low-energy electron microscopy (LEEM) and a scanning electron microscope (SEM), while the nanostructures formed were observed by atomic force microscope (AFM) method. Structural investigations were performed by low-energy electron diffraction (LEED) and X-ray photoelectron spectroscopy (XPS) methods. Our experiments confirmed a strong correlation between the formation of nanoislands and the presence of native oxide on silicon substrates.

Keywords: thin films; MBE; dewetting; thermal disintegration; gold nanostructures



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1. Introduction

Continuous solid metallic nanometric thin films can be meta- or unstable after deposition. This results in disintegration (dewetting) of the continuous layer into isolated nanoislands [1–4]. That process can be observed at temperatures well below the melting point of the nanolayer material, even at room temperature. The transformation mechanism is driven by a thermally accelerated diffusion that leads to the minimalization of surface free energy in the system [5–8]. Thin films, especially those with nanometer-scale thicknesses, have a relatively high surface energy and a large interfacial area with the substrate. This can create a driving force for the film to undergo dewetting and finally to form isolated islands. This process is governed by the balance between the energy required to create new surfaces (surface tension) and the energy gained by reducing the total interfacial area (interfacial tension) [9–12]. However, even if the thermodynamics of the system are known, there is no clear step-by-step explanation of the microscopic mechanism and kinetics of the dewetting process. Drawing from our previous experiments with gold, silver, and bimetallic Au-Ag alloyed nanostructures, we can explain the growth of metallic islands from thin films as the nucleation of holes on the film's surface [13–15]. These holes appear at the grain borders, preferably at the triple junctions, so-called T-junctions, of boundaries [16–19]. Additionally, the nucleation of holes can be accelerated by surface melting or premelting, especially in a

region with a lower film thickness at grain boundaries [20–22]. Then, a thin layer of liquid phase can cover solid grains, which accelerates disintegration of the thin film. The holes expand and the material diffuses, creating both raised ridges and grooves in the places of grain boundaries. These structures transform into isolated islands. Finally, as a result of melting, isolated and irregularly shaped islands evolve into spherical droplets. Melting occurs at temperatures well below the melting point of the bulk material, which is in line with theory [13,23,24]. It should be noted that the process can become more complicated when a eutectic forms between the thin film and the substrate. It could play a key role, especially in nanoscale systems, when both the eutectic temperature drastically decreases and the eutectic composition changes, with size reduction [25–28]. Disintegration of the metallic films could be a problem; for example, during electrical measurements in a wide range of temperatures, which was described by Wójcik et al. [29]. On the other hand, dewetting may lead to the formation of plasmonic nanostructures, so-called plasmonic platforms, which has been described in more detail in our previous publications [13,14,23,30–33].

In this work, we report the results of our study on the disintegration of gold thin film and the formation of nanoislands. To study the effect of silicon diffusion on processes of dewetting and the formation of gold nanostructures, thin metallic Au films were deposited on silicon substrates with and without the native oxide film. The disintegration process was measured in two ways: by analyzing SEM images of annealed samples, as well as during in situ measurements by LEEM and LEED.

2. Materials and Methods

The gold films were deposited by molecular beam epitaxy (MBE) method. For the future SEM measurements, two 2 inch Si wafers were first heated under a vacuum of 2×10^{-7} mbar in the load-lock chamber of the MBE system for 3 h at 150 °C to remove adsorbed gases. Next, one of them was annealed under UHV in the growth chamber at ~1000 °C to remove the native oxide film, as monitored by reflection high-energy electron diffraction (RHEED) technique (k-Space Associates, Inc., Dexter, MI, USA). When cooling the wafer to ~830 °C, the RHEED pattern changed from 1×1 to 7×7 , which clearly indicated a clean surface of Si(111) substrate. Then, deposition of nominally 2.8 nm thick Au film was performed on both substrates at room temperature with the rate of 0.6 nm/min. A resistively heated source of Au was used and pressure in the growth chamber during Au deposition was below 3×10^{-9} mbar. Moreover, evaporated thin films were annealed in a furnace for selected time and temperatures. The results of thermal treatment were observed by the FEI Quanta FEG 250 Scanning Electron Microscope (SEM) (FEI, Hillsboro, OR, USA) operating at 10 kV. For the in situ LEEM and LEED observations, a second series of gold films was prepared and deposited on two types of substrates: precleaned (111) silicon and silicon covered by native oxide. The Si(111) substrates had been flashed under UHV to obtain the clean surface characterized by the (7×7) reconstruction. The cleaning procedure was described in our previous work [34]. Silicon wafers with native oxide had been heated at 140 °C in order to remove absorbed water. The MBE deposition process was conducted at room temperature and a pressure below 3×10^{-10} mbar. Sixteen monolayers of Au were deposited from the resistively heated crucible with a rate of 0.2 mL/min. The evaporation rate had been determined during previous experiments [35] and additionally confirmed by the atomic force microscope (AFM) measurement on a step created by masking the thin film. The thickness and topography of the samples was investigated by Atomic Force Microscope NT-MDT Ntegra (NT-MDT, Moscow, Russia) in a contact mode. In situ observations of the thermal disintegration of the thin films were performed by the Elmitec Low-Energy Electron Microscope (LEEM) and the low-energy electron diffraction (LEED) (Elmitec, Clausthal-Zellerfeld, Germany) method during sample annealing under vacuum conditions. Additionally, X-ray photoemission spectroscopy (XPS) (ScientaOmicron, Uppsala, Sweden) and the energy-dispersive X-ray spectrometer (EDS) (Ametek, Berwin, PA, USA) for elemental analysis of nanoislands were used.

3. Results

The thickness of gold films evaporated on silicon substrate was determined by AFM method. The profile was measured on a step created by masking the film during evaporation. A typical profile line scan is presented in Figure 1. Measurements showed that an average thickness of 16 mL film is equal to ca. 3 nm. It should be noted that the thickness value is obtained by averaging several height profiles to eliminate random errors. Furthermore, it should be noted that we evaporated nominally 16 mL and the layer thickness is calculated by taking 1 mL Au as the distance between the (111) Au planes. This applies to the situation where Au is deposited on pure Si. On the other hand, in the case of an amorphous substrate the same amount of Au is evaporated, as on pure Si, but the Au grows in amorphous form. It is difficult to determine its thickness in relation to monolayers. It is also impossible to determine what orientation the layers are in during growth.

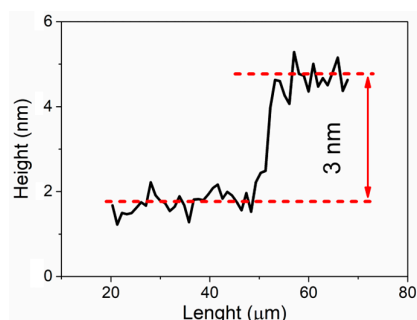


Figure 1. Thickness profile of 16 ML of Au evaporated on Si substrate.

The disintegration of the Au films was observed by LEEM. Measurements were performed in situ during heating under vacuum conditions. It was noticed that during heating grain boundaries appeared on a continuous film. When the temperature ramped from RT up to ca. 415 K the first voids were observed. Figure 2 shows an LEEM image of a hole at a multiple junction between grains. This could be considered as the beginning of the growth of voids and the starting point of the disintegration of the thin film. Our microscopic observation is perfectly in line with the literature data [16–19]. Further disintegration of the gold film is shown in the sequence of LEEM images presented in Figure 3. As seen, upon heating the voids grew and initially irregular-shaped gold structures were formed. Similar results were obtained by Świąch et al., during observations of the disintegration of submonolayer gold film on silicon [36]. The process of the formation of isolated gold or silver islands was described in detail in our previous work, e.g., in [13,14].

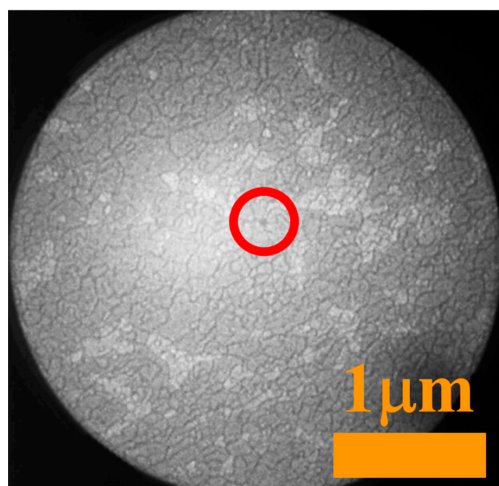


Figure 2. LEEM image of the 16 mL Au film evaporated on Si, annealed at 415 K. A void at a multiple junction between grains is marked by the circle.

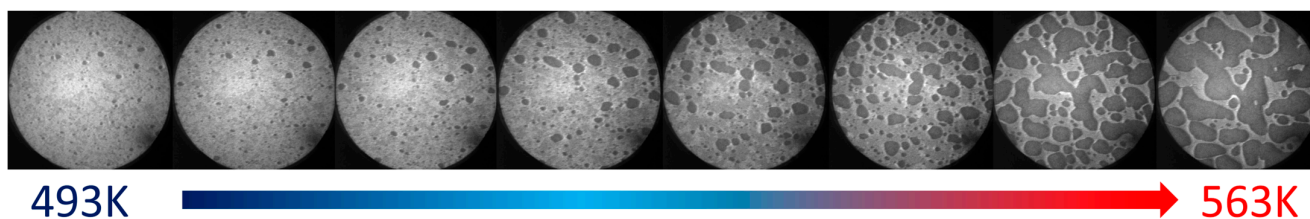


Figure 3. In situ LEEM images of the 16 mL Au film evaporated on a pure Si substrate, annealed at various temperatures. The diameter of the circular field is 5 μm .

The growth process and temperature-induced disintegration of the 16 ML thick Au layer deposited on both clean Si and native silicon oxide substrates were followed by low-energy electron diffraction (LEED). The large blurry spot observed in Figure 4a–d in the vicinity of the (0, 0) spot is associated with inelastic back-scattered electrons and can be considered as a background. Figure 4a shows a typical diffraction pattern of the Si(111)-(7×7) reconstructed surface that we observe for an atomically clean and well-ordered surface of Si(111). In contrast to the clean Si surface, the native silicon oxide reveals an amorphous character, which is manifested by the absence of diffraction spots (not shown). Figure 4b shows the diffraction pattern collected after deposition of the Au layer with a thickness of 16 mL on the clean Si substrate. During the deposition of the Au layer, a significant diffusion of Si atoms from the substrate across the Au layer is observed. The diffused Si atoms segregate on the surface of the Au layer, forming a weak-ordered low-buckled phase of silicene at room temperature, which is observed as a blurry ring of diffraction spots. In this case, heating the sample to a temperature close to the temperature of decomposition of the Au layer leads to the formation of a well-ordered planar phase of silicene on the top of the Au layer [37,38]. The formation of silicene in such a system is beyond the scope of this manuscript and a detailed description of such a process has been already published elsewhere [35]. The Au layer deposited on the amorphous native silicon oxide also has an amorphous character and again no diffraction spots are observed (not shown). Figure 4c,d show diffraction patterns collected after temperature-induced disintegration of the Au layer deposited on clean Si and native Si oxide, respectively. In the first case (4c), the hexagonal diffraction pattern corresponds to the Au-induced Si(111)-($\sqrt{3} \times \sqrt{3}$) surface reconstruction, which indicates that during the dewetting process of the Au layer some Au atoms remain on the Si surface, forming the ($\sqrt{3} \times \sqrt{3}$) reconstruction. Interestingly, in the second case, despite the amorphous character of Si oxide film, the same diffraction pattern of the Si(111)-($\sqrt{3} \times \sqrt{3}$) Au reconstructed surface is observed as in the previous case. The occurrence of the reconstructed silicon surface can be explained by the etching of silicon oxide, where Au droplets catalyze the decomposition of silicon dioxide, as reported here [39,40]. It also needs to be mentioned that for both cases only the Au-($\sqrt{3} \times \sqrt{3}$)-Si(111) diffraction pattern is observed. Thus, taking into account the strictly surface sensitivity of the LEED method, the surface of Au droplets is amorphous; hence, there are no extra diffraction spots. After slow cooling to RT, a transition from Si(111)-($\sqrt{3} \times \sqrt{3}$) to Si(111)-(6×6) reconstruction is observed for both considered cases (not shown).

A comparison of the mechanisms responsible for the formation of nanometric Au islands on clean and oxidized Si surfaces was performed by observation of the annealing effect with an SEM. Gold thin film that evaporated on both types of substrates were annealed in a furnace for 15 min at temperatures in a range of 500 K up to 800 K, then cooled and images were obtained with an SEM. A strong dependence of the surface morphology of the samples on the annealing temperature and type of substrate can be observed on the SEM images presented in Figure 5. Observations for structures formed on pure silicon substrates are in line with the previous ones, as presented in Figure 3. However, the presence of native silicon oxide film drastically changes the effects of annealing. The obtained structures are much smaller and more spherical than the islands grown on deoxidized Si. There are two possible explanations of these differences. First, the surface tensions of gold on silicon and

silicon oxide are different, which means that wetting by gold is more favorable on clean Si than on silicon oxide-covered substrate. In addition, the diffusion and exchange of species could be blocked at the Au-Si or Au-silicon oxide interface. It is well established already that enrichment of the gold film with silicon reduces the contact angle [41], which leads to the spreading of isolated Au-Si islands on the silicon surface instead of the formation of spherical droplets of pure gold.

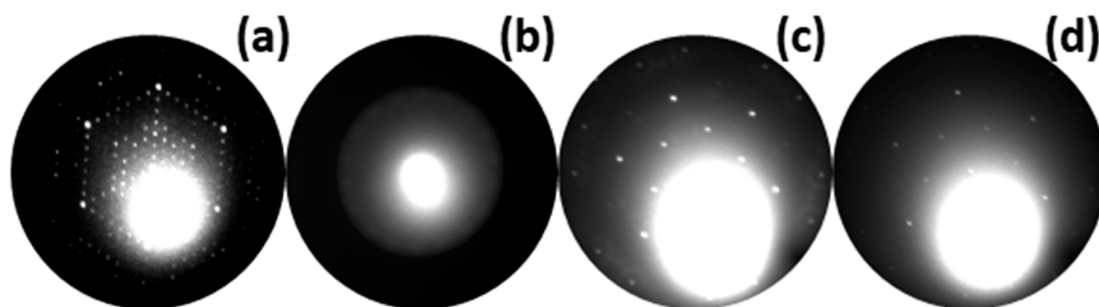


Figure 4. LEED pattern of Si(111)-(7 × 7) surface (a). Diffraction image collected after deposition of 16 mL thick Au layer on clean Si substrate (b). LEED patterns corresponding to disintegrated Au layer deposited on both clean Si surface (c) and native Si oxide (d), collected at temperatures of 663 K and 857 K, respectively. The images were collected with electron energy of 33.7 eV (a), 14.2 eV (b), and 45.7 eV (c,d).

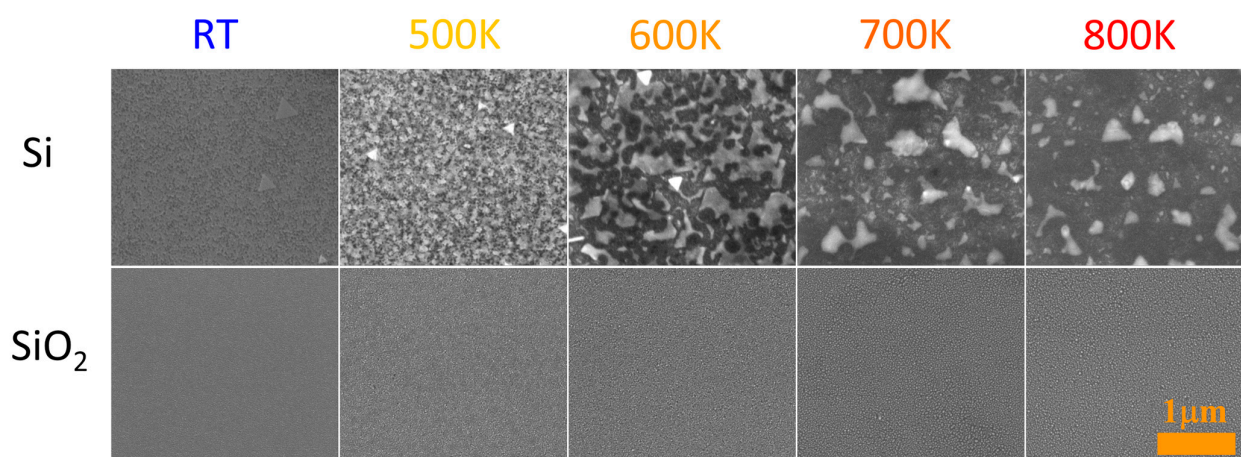


Figure 5. SEM images of the 3 nm gold film deposited on silicon (111) (top panel) and silicon oxide substrates (bottom panel) and annealed for 15 min at various temperatures. Additionally, images of as-deposited (RT) films are presented. The scale bar is the same for all images.

In order to highlight the differences resulting from the various states of substrate surfaces, Figure 6 shows high-magnification SEM images of Au deposit after 15 min long annealing at 600 K. Additionally, Figure 7 presents similar SEM images of Au films annealed at 600 K for various times, from 5 min up to 15 min. As seen, for films deposited on the pure silicon surface an increase in annealing temperature results in the formation of bigger and more visible structures. EDS point measurements confirmed a perfect removal and agglomeration in the formation of nanoislands of gold. Additionally, triangular islands are visible, as a result of lateral growth of Si (111) structures [41,42]. On the contrary, for samples prepared on native silicon oxide film their surface morphology does not change significantly and hemispherical nanostructures are well-visible for each annealing time (Figure 7). For comparison, in Figure 8a the AFM image of well-formed nanostructures on a substrate with native oxide is presented. Nanoislands were formed as a result of annealing the 16 ML film up to 857 K during LEEM and LEED observations in a vacuum chamber. The obtained hemispherical nanoislands uniformly covered the whole substrate's area. On

the basis of AFM investigations, the average size of nanostructures was found to be ca. 80 nm in both horizontal and vertical axes. A histogram with a size distribution is shown in Figure 8b. The histogram was prepared on the basis of AFM observations. It can be seen that the nanostructures are significantly larger than those presented in Figures 6 and 7. This is due to different annealing conditions. The heating rate was much smaller during LEEM and LEED observations than during annealing in a furnace. Finally, the heating took longer, which resulted in the formation of bigger structures. A strong influence of heating parameters on the size of nanostructures was observed in our previous works, e.g., [13–15].

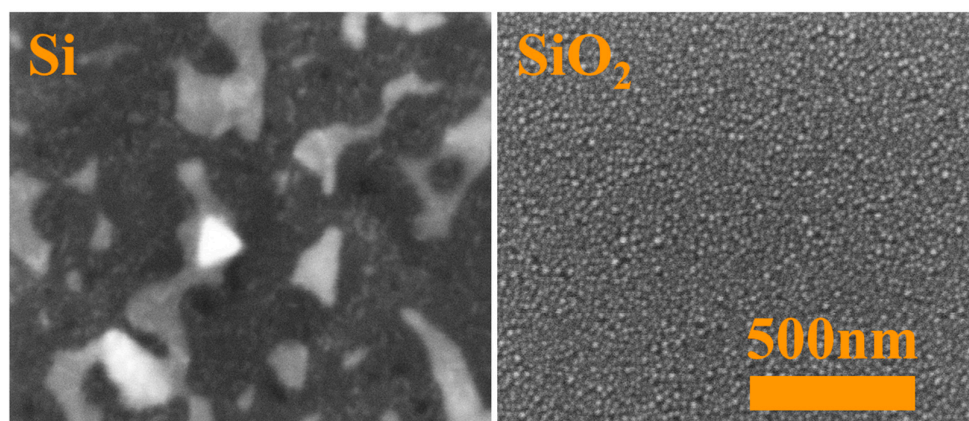


Figure 6. SEM images of the 3 nm gold film deposited on clean silicon (111) (left panel) and silicon with native silicon oxide (right panel) substrates annealed for 15 min at 600 K. The scale bar is the same for both images.

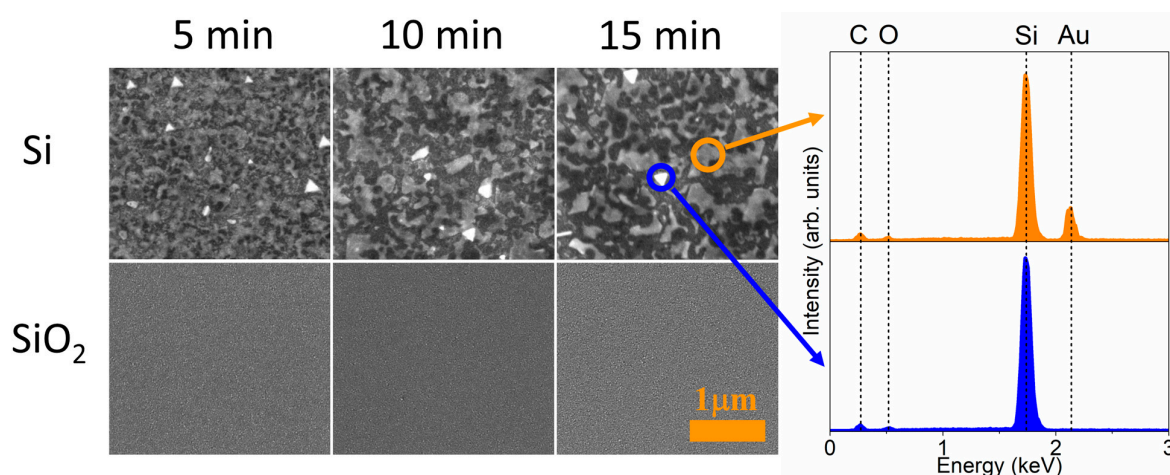


Figure 7. SEM images of the 3 nm gold film deposited on silicon (111) and silicon oxide substrates and annealed for 5, 10, and 15 min at 600 K (left panel). Additionally, EDS point spectra are presented (right panel).

XPS method was used to determine the chemical composition and valence state of silicon and gold in structures. Both types of samples were measured and then evaporated on pure silicon and silicon with silicon oxide. The spectra were measured immediately after Au deposition (Figure 9a,b) and after annealing at 600 K (Figure 9c,d). For each structure, two characteristic, for metallic gold, peaks at 83.9 eV and 87.6 eV are visible. They correspond to Au4f_{5/2} and Au 4f_{7/2} signals. The positions and shapes of Au4f peaks seem to be similar for as-deposited films (spectra a and b), while for annealed samples some asymmetry on the high-energy side (at ca. 90 eV) can be noticed. It could be related to the additional gold–silicide compound that appears on a surface [43,44]. Its presence is obvious for structures formed on clean silicon due to silicon diffusion from the substrate. The presence of the Au–Si compound in samples with native oxide film could be explained

by the etching of the oxide layer by liquid gold droplets, which was observed by LEED. Then, diffusion of Si atoms could start. Additionally, spectra for annealed samples exhibit the presence of a mix of metallic silicon and silicon oxide signals, with more metallic phases for structures evaporated on clean Si. The presence of silicon oxide in this case is related to fast oxidation in the air of the silicon surface.

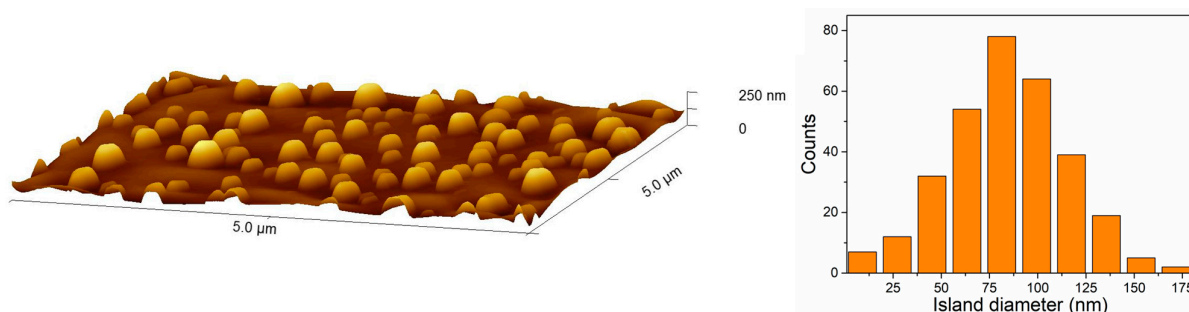


Figure 8. AFM image of isolated gold nanoislands on a silicon with native oxide formed by the annealing of metallic thin film at temperature 857 K (left panel) and the island size histogram calculated for the same sample (right panel).

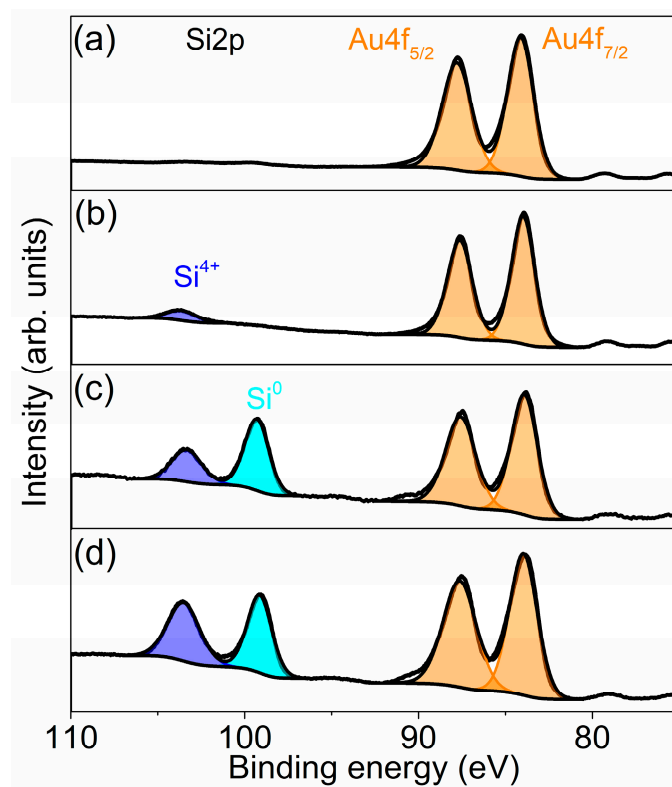


Figure 9. High-resolution XPS spectra for Si2p and Au4f regions, recorded for: (a) 16 mL of gold evaporated on silicon substrate, (b) 16 mL of gold evaporated on silicon substrate with native oxide, (c) nanostructures obtained as a result of annealing at 600 K for 15 min of gold film deposited on Si, (d) nanostructures obtained as a result of annealing at 600 K for 15 min of gold film deposited on Si with native oxide.

On the basis of measurements and performed analysis, we described the mechanism of the disintegration of metallic thin films. It could be concluded that the process of the disintegration of gold films occurs in various modes, depending on the substrate. In the case of pure silicon substrates, interface diffusion could play a bigger role in the process than for substrates with native silicon oxide. Silicon dioxide is a well-known and widely used diffusion barrier [45]. One of our important new findings is that only the etching

of the silicon oxide layer by liquid gold starts the diffusion process. It is reported that substrates play a key role in the process of the disintegration of metallic film, due to various surface tensions that have an influence on wetting properties [13,46,47]. The rate of diffusion must also be taken into account when considering the disintegration process. It is clear that the kind of substrate has an important influence on the size and shape of formed nanoislands. It could be useful for the design and production of optical active structures, where plasmonic properties strongly depend on nanostructure dimensions [48]. Our previous studies exhibit potential for tailoring plasmon resonance positions by change of, e.g., initial film thickness [13–15]. Here, we have shown an alternative method of changing the dimensions of the plasmonic structures.

4. Conclusions

Our microscopic and structural studies have shown various mechanisms of the disintegration of gold film on clean Si and Si with native oxide substrates. The disintegration of continuous film starts at a multiple grain junction, which was perfectly illustrated by LEEM. Thermal dewetting of the metallic thin films, deposited on both types of substrates, leads to the formation of nanoislands. However, the final shape and size of the islands strongly depends on the presence of native oxide on the silicon substrate. It was noticed that wetting of the deoxidized silicon substrate by gold is more preferable than in the case of the silicon oxide-covered substrate. Moreover, diffusion could be blocked by silicon oxide in the beginning phase of the disintegration of gold film. Additionally, an etching effect of the native silicon oxide by gold droplets was observed by LEED investigations and confirmed by XPS method.

Author Contributions: M.Ł.: conceptualization; methodology; data curation; formal analysis; investigation; validation; supervision; visualization; writing—original draft preparation; and writing—review and editing. P.D.: methodology; formal analysis; investigation; and writing—original draft preparation. M.G.: investigation. P.O.: investigation. J.K.: methodology and investigations. M.S.: methodology and investigations. A.P. investigation. Z.R.Z.: methodology and supervision. R.Z.: methodology and supervision. W.S.: supervision. B.K.: supervision and writing—review and editing. All authors have read and agreed to the published version of the manuscript.

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Conflicts of Interest: The authors declare no conflict of interest.

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